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**AS-IS CSC2 DETAILED PROCESS FLOW MODEL**

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Figure B.1 is a detailed representation of the Air Force's current, or *AS-IS*, CSC2 operational architecture, which is discussed in Chapter Three of this report. The process flow model was constructed from interviews with subject matter experts (see Appendix A for a list of interview participants); reviews of Air Force and joint-services doctrine, manuals, instructions, and CONOPs; analyses of lessons learned from the Air War Over Serbia; and insights from previous studies such as the AFFOR baselining exercise conducted by the Aerospace Command and Control, Intelligence, Surveillance, and Reconnaissance Center (AC2ISRC).

The figure emphasizes processes and information flows. Each box represents a process, with the arrows connecting the boxes representing products and other types of information inputs and outputs to the processes. In general, the processes flow sequentially from left to right, from peacetime training to redeployment. The iterative nature of many processes is represented by reverse arrows, which show progressively refined information inputs and outputs. Each process box is labeled with a primary reference indicating its source.

The vertical axis is a generalized view of the organizational structure of the Air Force and joint-services community, so that the height of a process in the diagram reflects the approximate organizational level at which it occurs. Although some processes are nominally associated with specific organizations, no attempt has been made to make such assignments systematically.

This graphic readily illustrates the relationships between processes and records the information flows between them. It also helps identify critical points in the system, such as processes that receive insufficient inputs, processes that require numerous inputs, and products that are required for numerous processes. Such critical points were used to help identify shortfalls in the *AS-IS* system and guide the development of the *TO-BE* concept.

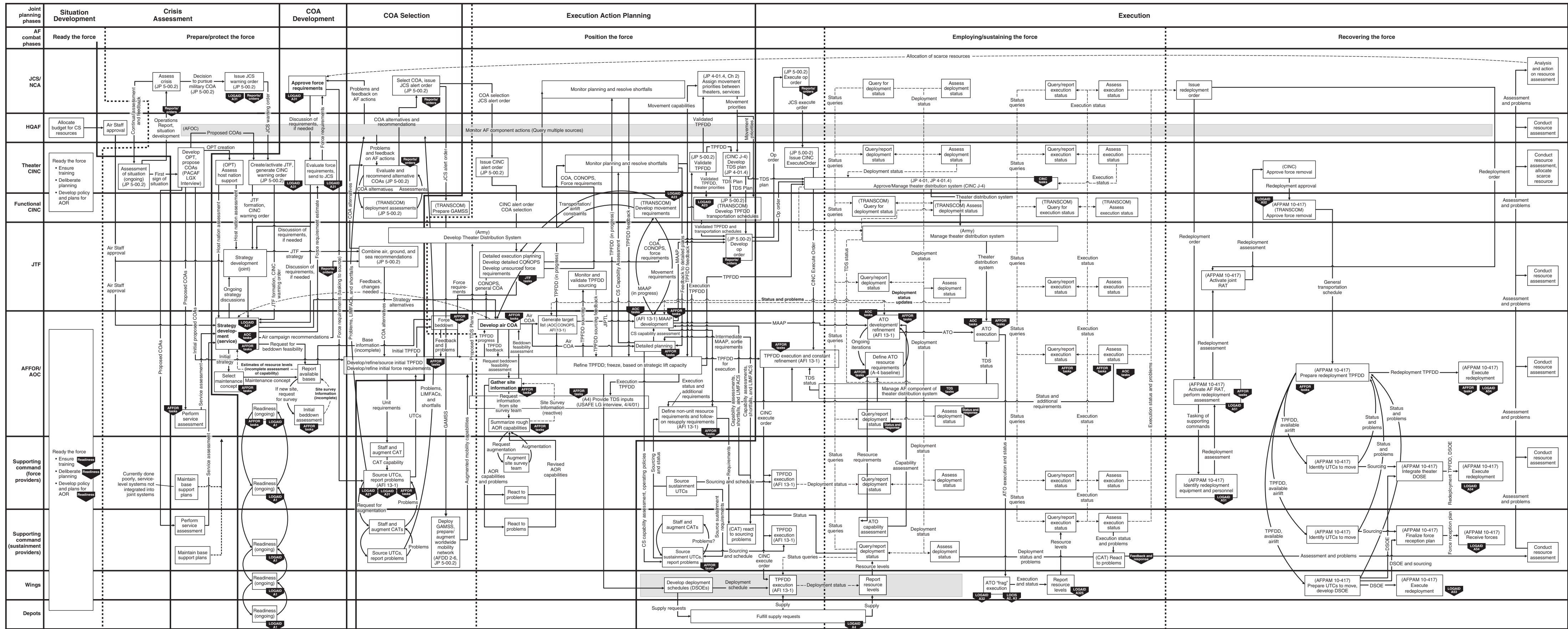


Figure B.1—AS-IS CSC2 Process Map